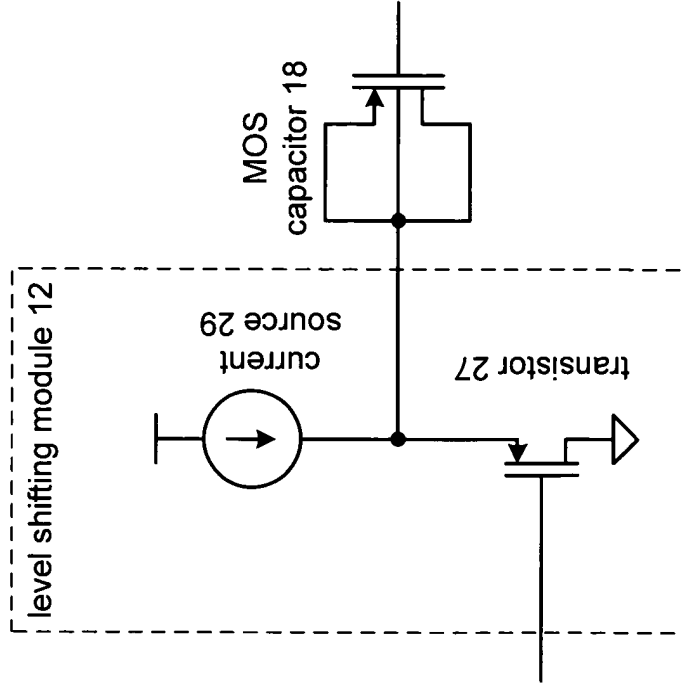
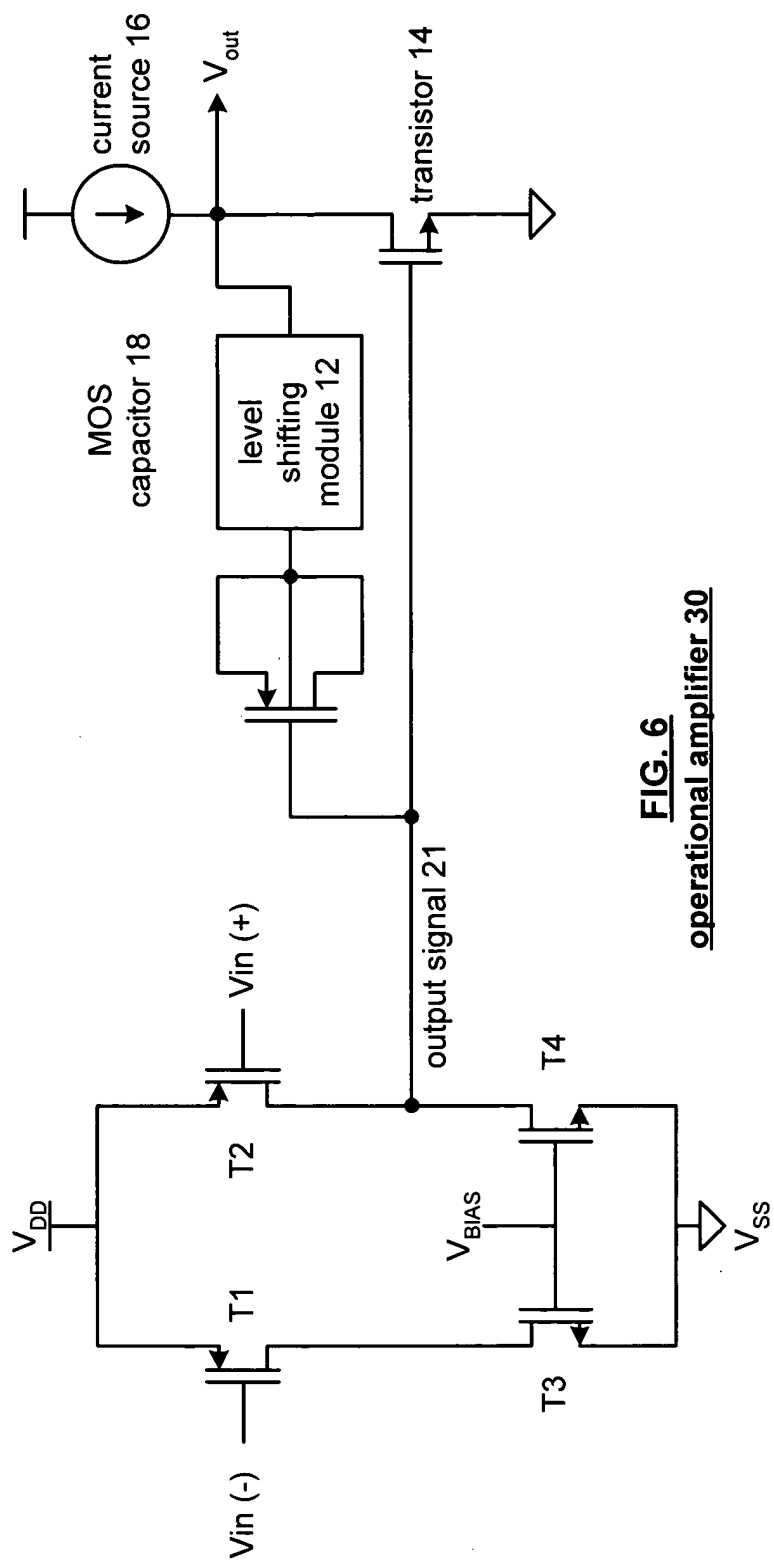


**FIG. 4**  
shift MOS cap to region A



**FIG. 5**  
shift MOS cap to region B



**FIG. 6**  
**operational amplifier 30**